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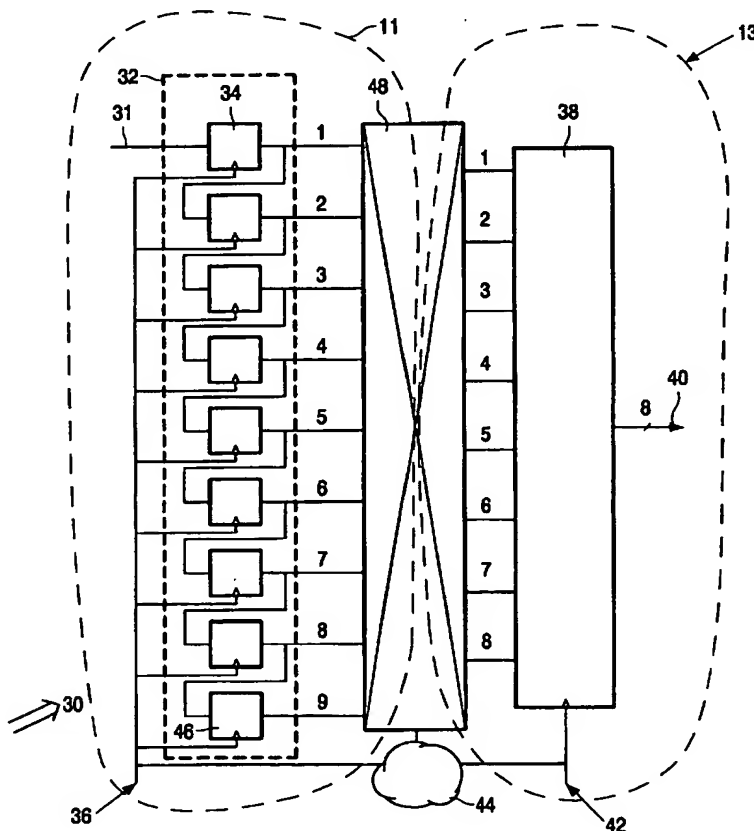
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(54) Title: **DEVICE FOR EXCHANGING DATA SIGNALS BETWEEN TWO CLOCK DOMAINS**



(57) Abstract: A device for transferring data signals between a first clock domain and a second clock domain comprises a serial memory element and a parallel memory element which are coupled. The serial memory element comprises at least one extra memory position more than the parallel memory element for the storage of the data signals.



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